

**ABSTRACT OF THE DISCLOSURE**

A method of recording and reproducing information is provided in which a recording area of a memory card is physically divided into small pages and is also partitioned into physical blocks each comprising a plurality of pages so that information is read and written in units of the blocks from and to the memory card. A logical address assigned to the physical block is added with a parity bit and is recorded in a replicated manner into each page within the block. In creating a logical/physical address translation table, all logical addresses are read from the memory card. When an error is detected in the logical addresses, a plurality of logical addresses recorded in a replicated manner into the same block as where the error is detected are read, and the logical address error is corrected based on a result of majority voting made for the plurality of read logical addresses.